



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Philip L. Hower, et al.

Docket No: TI-30010

Serial No: 10/036,323

Conf. No: 3224

Examiner: Thomas L. Dickey

Art Unit: 2826

Filed: 12/31/2001

For: N-CHANNEL LDMOS WITH BURIED P-TYPE REGION TO PREVENT PARASITIC BIPOLAR EFFECTS

EXTENSION OF TIME

Commissioner For Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(a)**

I hereby certify that the above correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on 11-12-03.

Ann Trent  
Ann Trent

Dear Sir:

Pursuant to 37 CFR 1.136(a), Applicants respectfully petition the Commissioner for an extension of the shortened statutory period for response in the above identified Application.

The fee for this extension is indicated below:

<input checked="" type="checkbox"/> One Month (\$110)	<input type="checkbox"/> Three Months (\$950)
<input type="checkbox"/> Two Months (\$420)	<input type="checkbox"/> Four Months (\$1,480)

Any further necessary extension of time is hereby requested. Charge any and all fees, or credit any overpayment, to the deposit account of Texas Instruments Incorporated, Account No. 20-0668. This form is submitted in duplicate.

11/20/2003 MBIZUNES 00000082 200668 10036323  
02 FC:1251 110.00 DA

Respectfully submitted,

Peter K. McLarty  
Attorney for Applicants  
Reg. No. 44,923

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